

**Amendment and Response Under 37 C.F.R. 1.116**

Applicant: Michael Bauer et al.

Serial No.: 10/789,033

Filed: February 27, 2004

Docket No.: I431.103.101/FIN 423 US

Title: ELECTRONIC COMPONENT AND SEMICONDUCTOR WAFER, AND METHOD FOR PRODUCING  
THE SAME

---

**REMARKS**

The following remarks are made in response to the Final Office Action mailed October 19, 2005. Claims 1-3 have been cancelled. Claims 1-15 were rejected. With this Response, claims 4-5 have been amended. Claims 4-15 remain pending in the application and are presented for reconsideration and allowance.

**Objections to the Drawings**

The Examiner objected to the drawings under 37 C.F.R. 1.83(a) because the drawings must show every feature of the invention specified in the claims, including the conductor tracks recited in claim 12. Conductor tracks 21 of the semiconductor chip 6 are on the top side 14, as described in the specification on page 10, lines 18-20, and as illustrated in Figure 2, labeled with reference marker 21. Consequently, Applicants believe the drawings are in proper form and the rejection should be withdrawn.

**Claim Rejections under 35 U.S.C. § 102(b) and (e)**

The Examiner rejected claims 1-3 under 35 U.S.C. § 102(b) as being anticipated by the Gebauer et al. U.S. Publication No. US 2002/0094607. Claims 1-3 are now cancelled rendering the rejection moot.

The Examiner rejected claims 6-11 under 35 U.S.C. § 102(e) as being anticipated by the Yamaguchi U.S. Publication No. US 2004/0157410.

The earliest effective date of the Yamaguchi U.S. Publication is its foreign priority date of January 16, 2003.

Accompanying this Response is a Declaration of Prior Invention under 37 C.F.R. 1.131 to establish conception of the subject matter of the present patent application prior to the effective reference date of January 16, 2003 of Yamaguchi, coupled with due diligence from prior to the effective reference date to the priority date of the present patent application (i.e., a constructive reduction to practice) on February 7, 2003. This Declaration is submitted to further

**Amendment and Response Under 37 C.F.R. 1.116**

Applicant: Michael Bauer et al.

Serial No.: 10/789,033

Filed: February 27, 2004

Docket No.: I431.103.101/FIN 423 US

Title: ELECTRONIC COMPONENT AND SEMICONDUCTOR WAFER, AND METHOD FOR PRODUCING THE SAME

---

prosecution of the present patent application and should not be construed as acquiescence by Applicants to the outstanding rejection. Accordingly, Applicants respectfully request consideration of the Declaration of Prior Invention under 37 C.F.R. 1.131.

For these reasons, Applicants submit that Yamaguchi does not qualify as a reference against Applicants' application under 35 U.S.C. 102(e). Accordingly, Applicants respectfully request that the rejection of claims 6-11 under Section 102 be reconsidered and withdrawn, and that claims 6-11 be allowed.

**Claim Rejections under 35 U.S.C. § 103**

The Examiner rejected claims 4 and 5 under 35 U.S.C. § 103(a) as being unpatentable over the Gebauer et al. U.S. Publication No. US 2002/0094607 in view of the Yamaguchi U.S. Publication No. US 2004/0157410.

The Examiner rejected claims 12-15 under 35 U.S.C. § 103(a) as being unpatentable over the Yamaguchi U.S. Publication No. US 2004/0157410 in view of the Glenn et al. U.S. Patent No. 6,406,934.

Applicants have amended claims 4 and 5 to incorporate their base claims and, as such, for substantially the same reasons as previously presented for the patentability of claims 4-5 and 12-15, Applicants submit that Yamaguchi does not qualify as a reference against Applicants' application under 35 U.S.C. § 102(e) and therefore does not qualify as a reference against Applicants' claims 4-5 and 12-15 under 35 U.S.C. § 103.

Therefore, Applicants respectfully request reconsideration and withdrawal of the 35 U.S.C. § 103(a) rejection to claims 4, 5 and 12-15, and request allowance of these claims.

**CONCLUSION**

In view of the above, Applicant respectfully submits that pending claims 4-15 are in form for allowance and are not taught or suggested by the cited references. Therefore, reconsideration and withdrawal of the rejections and allowance of claims 4-15 is respectfully requested.

**Amendment and Response Under 37 C.F.R. 1.116**

Applicant: Michael Bauer et al.

Serial No.: 10/789,033

Filed: February 27, 2004

Docket No.: I431.103.101/FIN 423 US

Title: ELECTRONIC COMPONENT AND SEMICONDUCTOR WAFER, AND METHOD FOR PRODUCING  
THE SAME

No fees are required under 37 C.F.R. 1.16(h)(i). However, if such fees are required, the Patent Office is hereby authorized to charge Deposit Account No. 50-0471.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Any inquiry regarding this Amendment and Response should be directed Paul P. Kempf at Telephone No. (612) 767-2502, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:

**Dicke, Billig & Czaja, PLLC**  
Fifth Street Towers, Suite 2250  
100 South Fifth Street  
Minneapolis, MN 55402

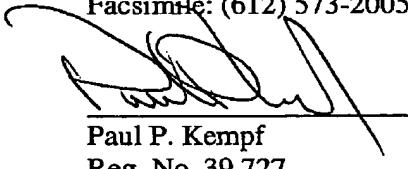
Respectfully submitted,

Michael Bauer et al.,

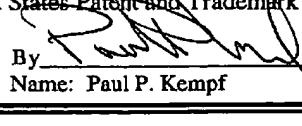
By their attorneys,

**DICKE, BILLIG & CZAJA, PLLC**  
Fifth Street Towers, Suite 2250  
100 South Fifth Street  
Minneapolis, MN 55402  
Telephone: (612) 767-2502  
Facsimile: (612) 573-2005

Date: 12/22/05  
PPK:cmj

  
Paul P. Kempf  
Reg. No. 39,727

**CERTIFICATE UNDER 37 C.F.R. 1.8:** The undersigned hereby certifies that this paper or papers, as described herein, are being facsimile transmitted to the United States Patent and Trademark Office, Fax No. (571) 273-8300 on this 12/22/05 day of December, 2005.

  
By \_\_\_\_\_  
Name: Paul P. Kempf